

Atty Dock t N . MCRN-F5000**DECLARATION FOR PATENT APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

APPARATUS AND METHOD FOR REDUCING PROGRAMMING CYCLES FOR MULTISTATE MEMORY SYSTEM

the specification of which (check one) X is attached hereto or ___ was filed on ___ as Application No. ___ and was amended on ___ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed
Yes No

Number Country Day/Month/Year Filed

Number Country Day/Month/Year Filed

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) below.

Application Number Filing Date

Application Number Filing Date

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application Number Filing Date Status: Patented, Pending, Abandoned

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like made are punishable by fine or imprisonment or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor ROBERT D. NORMAN

Inventor's signature

Robert D. Norman

Date

10/10/96

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Full name of second joint inventor, if any, _____

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Date _____

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Full name of fourth joint inventor, if any, _____

Inventor's signature _____

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Citizenship _____

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Full name of fifth joint inventor, if any, _____

Inventor's signature _____

Date _____

Residence _____

Citizenship _____

Post Office Address _____

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S/N 08/730,099

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert D. Norman

Examiner: T. Thai

Serial No.: 08/730,099

Group Art Unit: 2752

Filed: October 15, 1996

Docket: 703.067US1

Title: APPARATUS AND METHOD FOR REDUCING PROGRAMMING CYCLES
FOR MULTISTATE MEMORY SYSTEM

REVOCATION AND POWER OF ATTORNEY

Assistant Commissioner for Patents
Washington, D.C. 20231

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05, 402.07, and 324
please revoke any existing Powers of Attorney, if any, and appoint the following attorneys and/or
patent agents to prosecute this application and to transact all business in the Patent and
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Reg. No. P-41,615
Reg. No. 39,610
Reg. No. 32,836
Reg. No. 35,075
Reg. No. 40,925
Reg. No. 38,107
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Reg. No. 32,651
Reg. No. 35,138
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Reg. No. 36,198
Reg. No. 30,568
Reg. No. 30,871
Reg. No. P-42,858
Reg. No. 34,095
Reg. No. 25,816
Reg. No. 39,838
Reg. No. 31,884
Reg. No. 37,748
Reg. No. 30,440

Serial Number: 08/730,099

Filing Date: October 15, 1996

Title: APPARATUS AND METHOD FOR REDUCING PROGRAMMING CYCLES FOR MULTISTATE MEMORY SYSTEM**CERTIFICATE UNDER 37 CFR §3.73(b)**

Micron Technology, Inc. hereby certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of an assignment from the inventor filed October 15, 1996 and recorded on Reel 8269, Frame 0766. To the best of my knowledge and belief, title is in Micron Technology, Inc., the assignee.

Pursuant to 37 C.F.R. §3.73(b) I hereby declare that I, Michael L. Lynch, am empowered to sign this certificate on behalf of Micron Technology, Inc., the assignee.

Please note that Micron Quantum Devices, Inc. has merged into Micron Technology, Inc. as evidenced by the attached Certificate of Merger.

I hereby declare that all statement made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true.

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Date

9/2/98

By


Michael L. Lynch

Title: Chief Patent Counsel